

<b>Notice of References Cited</b>	Application/Control No. 10/749,938	Applicant(s)/Patent Under Reexamination SWAFFORD ET AL.	
	Examiner Pierre-Michel Bataille	Art Unit 2186	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-7,133,993	11-2006	Jackson et al.	711/172
*	B	US-7,124,251	10-2006	Clark et al.	711/170
*	C	US-6,985,976	01-2006	Zandonadi et al.	711/170
*	D	US-6,658,652	12-2003	Alexander et al.	711/100
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Data Structure and Insertion Algorithm for Representing Asynchronous Occurrences for Visualization by Trace Visualization Tools using Ghant Charts with Occurrence Hierarchies; International Business Machines Technical Disclosure Bulletin; Vol 36, No 07, Jul. 1993; pp. 547-557.
*	V	Petit-Bianco, Alexandre, "Java Garbage Collection for Real-Time Systems", Dr. Dobb's Journal Oct. 1998. <a href="http://www.ddj.com/documents/s=915/ddj9810a/9810a">www.ddj.com/documents/s=915/ddj9810a/9810a</a> .
*	W	Interrupt Driven Task Scheduler for Systems, IBM Technical Disclosure Bulletin, Mar. 1992.
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.